

**WYKAZ PUBLIKACJI PRACOWNIKÓW KATEDRY MIKROELEKTRONIKI I  
TECHNIK INFORMATYCZNYCH  
1996**

**MONOGRAFIE**

1. Andrzej NAPIERALSKI, Marek TUROWSKI (Editors): „MIXED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS” - *Proceedings of the 3rd Advanced Training Course: „Education of computer aided design of modern ICs and devices”, MIXDES'96, Technical University of Łódź, Poland, 30 may - 1 june 1996, 644 strony, (ISBN 83-906066-0-7).*
2. Zygmunt CIOTA: „Teoria i praktyczna realizacja analogowych filtrów scalonych ze szczególnym uwzględnieniem filtrów o skończonej odpowiedzi impulsowej”, *Zeszyty Naukowe nr 757, Rozprawy naukowe, z.231, Łódź 1996 (ISSN 0137-4834). Rozprawa habilitacyjna*

**ROZDZIAŁ W KSIĄŻCE WYD. ZA GRANICĄ**

1. Andrzej NAPIERALSKI: „Microtechnology - Thermal problems in Micromachines, ULSI & Microsensors Design”, **Book:** *Future Trends in Microelectronics. Kluwer Academic Publishers, Science and Technology Division, The Netherlands, 1996, pp. 391-396.*
2. Marek TUROWSKI, Andrzej NAPIERALSKI: “Teaching Design of Integrated Microsystems at the Technical University of ŁÓDŹ”, **Book:** *Microelectronics education”(Editors: Georges KAMARINOS, Nadine GUILLEMOT, Bernard CURTOIS; World Scientific, Singapore-New Jersey-London-Hong Kong, 1996, pp.61-64, (ISBN 981-02-2653-5).*

**ROZDZIAŁY W KSIĄŻCE WYD. W KRAJU**

1. Andrzej NAPIERALSKI: „Komputerowe metody analizy i projektowania układów półprzewodnikowych”, *ELEKTRONIKA Łódź 1995/1996, ss.69-98.(ISBN-83-86453-78-8).*
2. **Andrzej Napieralski**, Małgorzata Napieralska, Mariusz Grecki: "COMPUTER LABORATORY FOR CAD OF POWER AND INTEGRATED CIRCUITS.", *Computer-aided design and computer-aided engineering in electronic engineering education - pod redakcją Andrzeja FILIPKOWSKIEGO, Publishing house of the Warsaw University of Technology, Warszawa 1966, pp 135-184.*
3. **Andrzej Napieralski**, Wojciech Wójciak: „INTRODUCTION TO CIRCUIT SIMULATION WITH CADENCE”, *Computer-aided design and computer-aided engineering in electronic engineering education - pod redakcją Andrzeja FILIPKOWSKIEGO, Publishing house of the Warsaw University of Technology, Warszawa 1966, pp 207-233.*
4. **Andrzej Napieralski**, Wojciech Wójciak: Appendix E; „THE DESIGN RULES FOR CADENCE”, *Computer-aided design and computer-aided engineering in electronic engineering education - pod redakcją Andrzeja FILIPKOWSKIEGO, Publishing house of the Warsaw University of Technology, Warszawa 1966, pp 267-271.*

**ARTYKUŁY W CZASOPISMACH ZAGRANICZNYCH**

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2. Zygmunt CIOTA, A. NAPIERALSKI, J.L. NOULLET: “Analogue realisation of integrated FIR filters”. *IEE Proc.-Circuits Devices Syst., Vol.143, No. 5. October 1996.*

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1. red.: Wiesław KUŹMICZ, Andrzej PFITZNER, Andrzej WIELGUS, Antoni FIJAŁKOWSKI, Tadeusz ŁUBA, Krzysztof JASIŃSKI, **Marek TUROWSKI**, Andrzej KOS: "Układy ASIC w małych i średnich firmach. Formy wsparcia i promocji", *Prace Instytutu Technologii Elektronowej* 1996, z.4/5, pp. 23-58.

## ARTYKUŁY W CZASOPISMACH PAN

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2. FURMAŃCZYK M., NAPIERALSKI A. : "Application of Sparse Matrix Techniques and Self-adopting Mesh Generation to 3-D Transient Thermal Simulation", *Biuletyn Polskiej Akademii Nauk*, Vol.44, No.2, 1996, pp. 185-191.

## ARTYKUŁY W POZOSTAŁYCH CZASOPISMACH

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